

TELECOM QUALITY 2000VA SINE WAVE INVERTER SPI 2000 SERIES

- Modular design, light weight
- Sinusoidal wave shape
- 2000VA output power
- Full electronic protection
- Field-proven design topology



The SPI 2000 Series DC/AC inverter uses established design techniques to ensure high reliability. Suitable for a wide range of applications, the SPI 2000 features full electronic protection, high efficiency and low output noise. The built-in fan provides sufficient airflow for operation without de-rating up to 50°C ambient temperature. Extended operating temperature (-40 to +65°C) is available. The inverter can be loaded with a fluorescent lamp load up to the full specified output power.

SPECIFICATIONS

Input Voltage

24V, 36V, 48V, 125V, 250VDC
+/-20% are standard
Consult Snaptec for other inputs

Input Protection

Thermal fuse
Inrush current limiting
Reverse polarity protection

Standards

Designed to meet
C22.2 No. 107.1 - 01,
UL 458 and EN60950

EMI

EN 55022 Class B for versions
where input current <70A.
Class B filtering is an option
where input current >70A.

Output Voltage

115VAC/17.4A continuous or
230VAC/8.7A continuous at
50, 60 or 400Hz with grounded
neutral.

Isolated floating output optional.
Consult Snaptec for other output
requirements

Wave Form

Sinusoidal

Total Harmonic Distortion

Less than 5% at full load

Efficiency

Min 78% at full load

Line Regulation

Maximum 0.5%

Load Regulation

Maximum ± 2% from no load
to full load.

Output Protection

Current limiting with short circuit
protection
Thermal shutdown with automatic
recovery in case of continuous
overload or insufficient airflow

Load Crest Factor

Maximum 3.0 at 90% load

Operating Temperature Range

0° C to +50° C

Temperature Drift

0.05% per °C over operating
temperature range

Humidity

5 - 95% non-condensing

Dimensions

4U" X 19" x 15" enclosed case
(H x L x W)

Connections

Input/output: terminal block

Weight

40 pounds (18.2 kg)

Options:

Output Fail Alarm (Form C)
Remote Inhibit: By connecting
DC voltage (as specified) to the
inhibit terminals